

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	389	712/226.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:07
L2	243963	flash	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:07
L3	33796	dma	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:07
L4	45755	sram	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:07
L5	7807	2 and 3	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:08
L6	2367	4 and 5	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:08
L7	572513	microprocessor or microcomputer or (integrated adj circuit)	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:08
L8	1796	6 and 7	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:10
L9	492	2 with 3	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:10
L10	106	4 with 9	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:10
L11	87	7 and 10	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:30
L12	14877	charge adj pump	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:30
L13	774	2 with 12	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:30
L14	243	2 near3 12	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:31
L15	12145	data adj port	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:31
			US-PGPUB; USPAT; EPO			

## EAST Search History

L16	492	(instruction or code) adj port	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:32
L17	181	15 and 16	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:32
L18	1	14 and 17	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:33
L19	1248	embedd\$5 near3 flash	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:34
L20	41	8 and 19	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 16:37
L21	10	12 with 19	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:09
L22	87766	(interface or path) near3 (exten\$5 or widen\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:12
L23	156	2 with 22	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:12
L24	46	2 near3 22	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:17
L25	457	flash with slave	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:19
L26	0	slave near2 interface with 15	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:18
L27	156	flash near3 slave	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:20
L28	530	coprocessor near3 interface	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:20
L29	0	flash near3 28	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:20
L30	6	flash with 28	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:21

## EAST Search History

L31	6834	configuration adj register\$1	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:24
L32	892	flash with fpga	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:43
L33	0	coprocessor with 32	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:43
L34	22	coprocessor and 32	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 17:59
L35	2	configuration adj register\$1 and 34	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:11
L36	46619	variable adj (width or length)	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:12
L37	12034	7 and 36	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:12
L38	2375	37 and flash	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:12
L39	238	6 and 38	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:12
L40	69	39 and fpga	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:12
L41	1426	variable adj (width or length) near3 instruction\$1	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:13
L42	0	40 and 41	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:13
L43	15	41 and fpga	US-PGPUB; USPAT; EPO	OR	OFF	2006/03/17 18:13